# Power MOSFET and **Schottky Diode**

## 30 V, 5.7 A, Single N-Channel with 30 V, 2.8 A, Schottky Barrier Diode Features

### • FETKY<sup>™</sup> Surface Mount Package Saves Board Space

- Independent Pin-Out for MOSFET and Schottky Allowing for **Design Flexibility**
- Low R<sub>DS(on)</sub> MOSFET and Low V<sub>F</sub> Schottky to Minimize Conduction Losses
- Optimized Gate Charge to Minimize Switching Losses
- This is a Pb-Free Device

### Applications

- Disk Drives
- DC-DC Converters
- Printers

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<b>MOSFET MAXIMUM RATINGS</b> (T <sub>J</sub> = 25°C unless otherwise stated)							
Ratir	Symbol	Value	Unit				
Drain-to-Source Voltage	V <sub>DSS</sub>	30	V				
Gate-to-Source Voltage	)		V <sub>GS</sub>	±20	V		
Continuous Drain		T <sub>A</sub> = 25°C	۱ <sub>D</sub>	4.7	А		
Current $R_{\theta JA}$ (Note 1)		T <sub>A</sub> = 70°C		3.8			
Power Dissipation $R_{\theta JA}$ (Note 1)		T <sub>A</sub> = 25°C	PD	1.6	W		
Continuous Drain		T <sub>A</sub> = 25°C	I <sub>D</sub>	3.3	А		
Current $R_{\theta JA}$ (Note 2)	Steady	T <sub>A</sub> = 70°C		2.6			
Power Dissipation $R_{\theta JA}$ (Note 2)	State	T <sub>A</sub> = 25°C	PD	0.77	W		
Continuous Drain		T <sub>A</sub> = 25°C	I <sub>D</sub>	5.7	А		
Current R <sub>0JA</sub> t < 10 s (Note 1)		T <sub>A</sub> = 70°C		4.5			
Power Dissipation $R_{\theta JA} t < 10 s (Note 1)$		T <sub>A</sub> = 25°C	P <sub>D</sub>	2.3	W		
Pulsed Drain Current		= 25°C, = 10 μs	I <sub>DM</sub>	19	A		
Operating Junction and Storage Temperature			T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		
Source Current (Body D	iode)		I <sub>S</sub>	1.3	А		
Lead Temperature for So (1/8" from case for 10 s)		urposes	ΤL	260	°C		

### SCHOTTKY MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	30	V	
DC Blocking Voltage		V <sub>R</sub>	30	V
Average Rectified Forward Current, (Note 1)			2.8	A
	t < 10 s		4.1	



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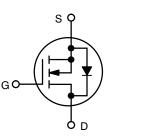
### http://onsemi.com

### **N-CHANNEL MOSFET**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max
30 V	48 mΩ @ 10 V	5.7 A
	70 mΩ @ 4.5 V	•••••

### SCHOTTKY DIODE

V <sub>R</sub> Max	V <sub>F</sub> Max	I <sub>F</sub> Max
30 V	0.5 V	2.8 A

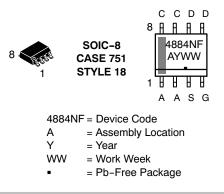




**N-Channel MOSFET** 

Schottky Diode

#### **MARKING DIAGRAM & PIN ASSIGNMENT**



### **ORDERING INFORMATION**

Devi	се	Package	Shipping <sup>†</sup>
NTMD4884	NFR2G	SOIC-8 (Pb-Free)	2500/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter MOSFET & Schottky	Symbol	Max	Unit
Junction-to-Ambient – Steady State (Note 1)	$R_{ hetaJA}$	79	
Junction-to-Ambient – t ≤10 s Steady State (Note 1)	$R_{ hetaJA}$	54	°C/W
Junction-to-FOOT (Drain) Equivalent to $R_{\theta JC}$	$R_{\thetaJF}$	50	°C/vv
Junction-to-Ambient – Steady State (Note 2)	$R_{\theta JA}$	163	

Surface-mounted on FR4 board using 1 inch sq pad size, 1 oz Cu.
Surface-mounted on FR4 board using the minimum recommended pad size.

### ELECTRICAL CHARACTERISTICS (T = 25°C unless otherwise noted)

Characteristic	Symbol	Test Condition		Min	Тур	Мах	Unit
OFF CHARACTERISTICS						•	
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu$ A		30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>				24		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 24 V	T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C			1.0 20	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V	-			±100	nA
ON CHARACTERISTICS (Note 3)	.033	.03 0 1,1	630 !				
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I	ь — 250 µA	1.0		2.5	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>	VGS - VDS, I	D = 200 μΑ	1.0	5.0	2.0	mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 4.0 A		34	48	111 <b>v</b> / C
	· ·DS(on)	$V_{GS} = 4.5 V$	$I_{\rm D} = 3.5 \rm{A}$		50	70	mΩ
Forward Transconductance	<b>g</b> fs	$V_{DS} = 5.0 V_{c}$	5		10		S
Gate Resistance	R <sub>G</sub>	. 53			2.4	3.6	Ω
CHARGES, CAPACITANCES AND GATE RE	5						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1.0 MHz, V <sub>DS</sub> = 15 V			280	360	
Output Capacitance	C <sub>OSS</sub>				60	80	pF
Reverse Transfer Capacitance	C <sub>RSS</sub>				32	42	
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 15 V,			2.8	4.2	
Threshold Gate Charge	Q <sub>G(TH)</sub>				0.4		
Gate-to-Source Charge	Q <sub>GS</sub>	I <sub>D</sub> = 4	.0 Å		1.2		nC
Gate-to-Drain Charge	Q <sub>GD</sub>				1.0		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 10 V, V I <sub>D</sub> = 4			5.6	8.0	nC
SWITCHING CHARACTERISTICS (Note 4)					•		
Turn-On Delay Time	t <sub>d(ON)</sub>				6.0	12	
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V, V	/ <sub>DS</sub> = 15 V,		6.5	13	ns
Turn-Off Delay Time	t <sub>d(OFF)</sub>	I <sub>D</sub> = 1.0 A, F	$R_{\rm G} = 6.0 \Omega$		14	26	
Fall Time	t <sub>f</sub>	1			1.4	7.0	
DRAIN-TO-SOURCE CHARACTERISTICS				-			
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V	T <sub>J</sub> = 25°C		0.8	1.0	V
		I <sub>D</sub> = 1.3 A	T <sub>J</sub> = 125°C		0.65		
Reverse Recovery Time	t <sub>RR</sub>		-		9.2	20	
Charge Time	t <sub>a</sub>	$V_{GS} = 0 V, d_{IS}/d_{IS}$	d <sub>t</sub> = 100 Α/μs,		6.0		ns
Discharge Time	t <sub>b</sub>	I <sub>S</sub> = 4	.0 A		3.2		
Reverse Recovery Time	Q <sub>RR</sub>				3.3		nC

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

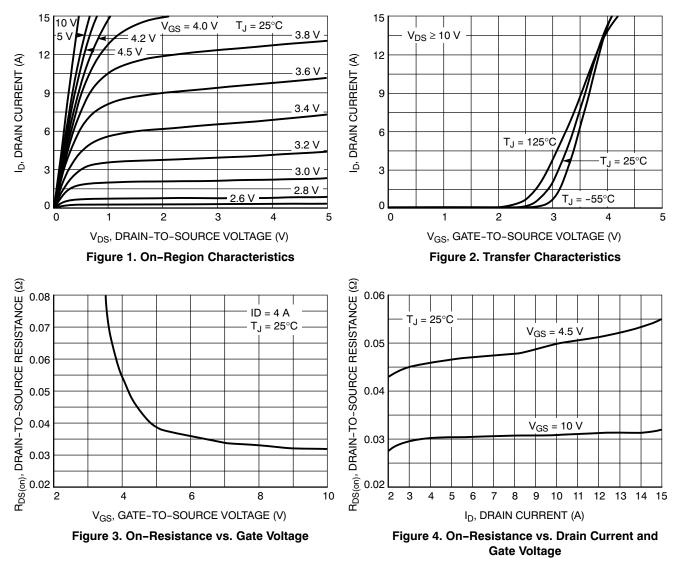
Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit

#### SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Test Con	ditions	Min	Тур	Max	Unit
Maximum Instantaneous	V <sub>F</sub>	I <sub>F</sub> = 0.1 A	$T_J = 25^{\circ}C$		0.26	0.28	V
Forward Voltage			T <sub>J</sub> = 125°C		0.11	0.13	
		I <sub>F</sub> = 2.0 A	$T_J = 25^{\circ}C$		0.4	0.50	
			T <sub>J</sub> = 125°C		0.35	0.46	
Maximum Instantaneous	I <sub>R</sub>	V <sub>R</sub> = 10 V	$T_J = 25^{\circ}C$		0.020	0.25	mA
Reverse Current			T <sub>J</sub> = 125°C		10	37	

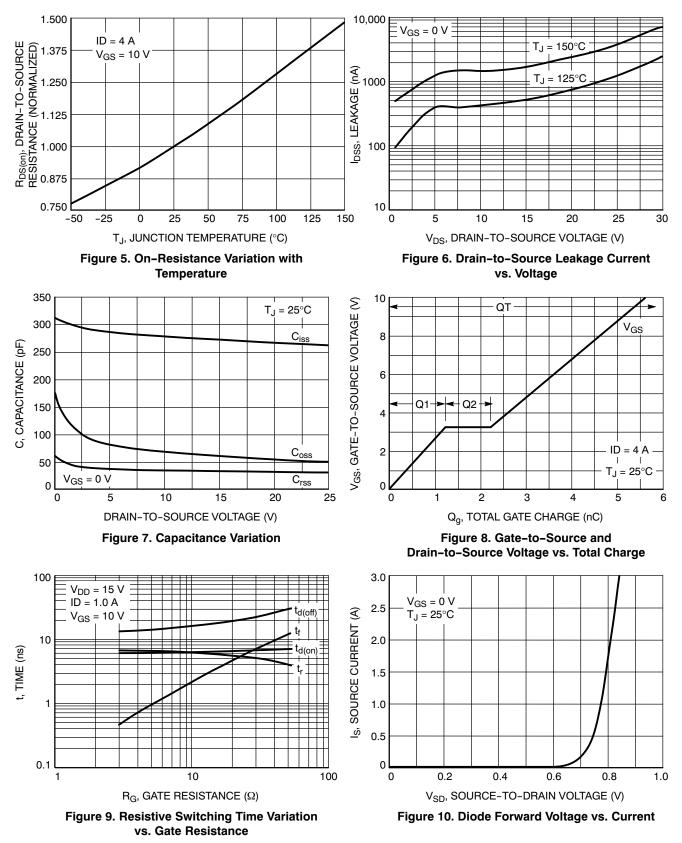
3. Pulse Test: pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2%.

4. Switching characteristics are independent of operating junction temperatures.

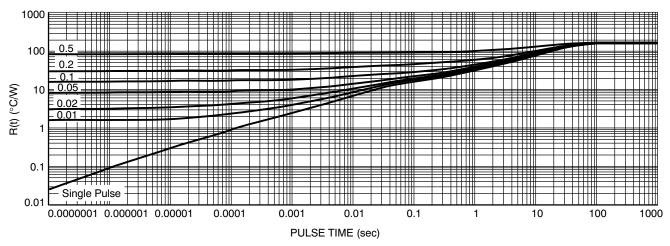


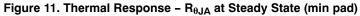
## **TYPICAL CHARACTERISTICS**

### **TYPICAL CHARACTERISTICS**









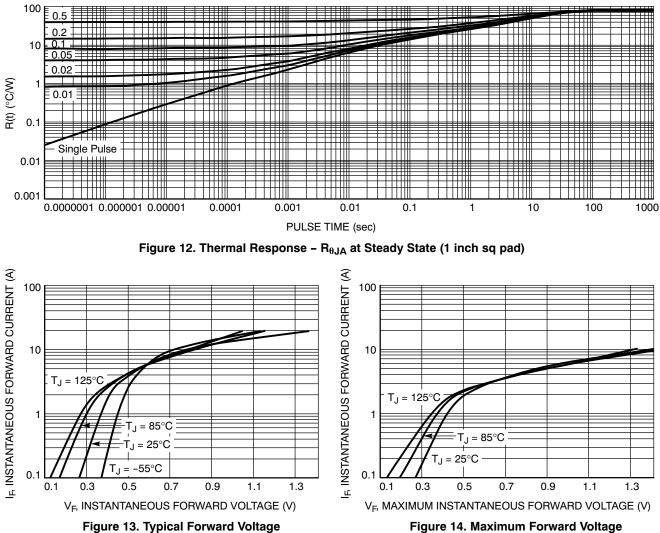
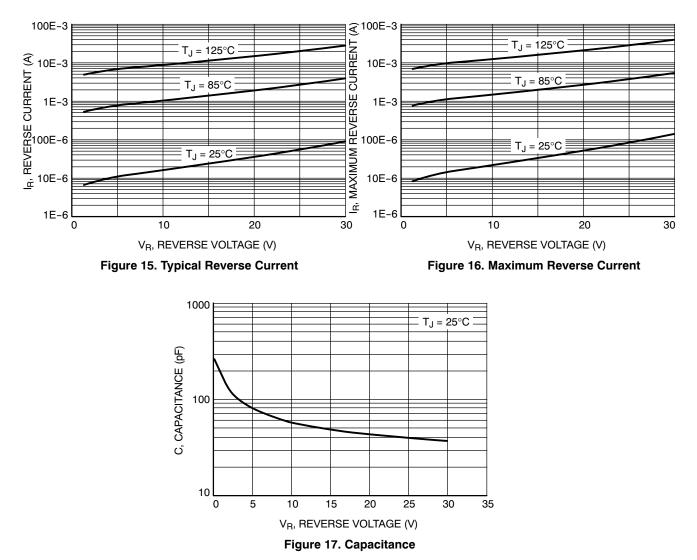


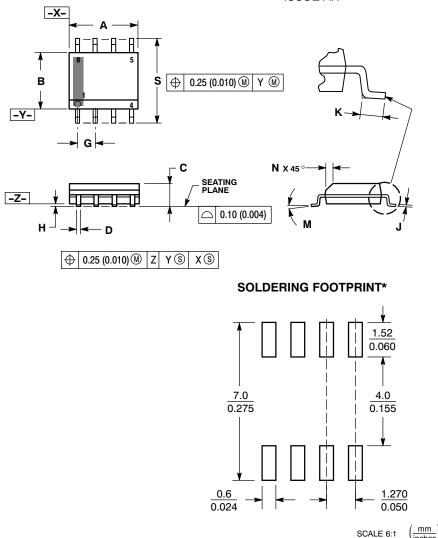
Figure 14. Maximum Forward Voltage

### **TYPICAL CHARACTERISTICS**



#### PACKAGE DIMENSIONS

SOIC-8 NB CASE 751-07 ISSUE AH



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER. 2
- DIMENSION A AND B DO NOT INCLUDE З.
- MOLD PROTRUSION MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE
- DIMENSION D DOES NOT INCLUDE DAMBAR 5. PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
- 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07. 6.

	MILLIN	IETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
в	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27	7 BSC	0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
ĸ	0.40	1.27	0.016	0.050
м	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

STYLE 18: PIN 1. ANODE

2 ANODE SOURCE

З.

4 GATE 5 DRAIN

DRAIN 6

- CATHODE 7. 8 CATHODE

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